DPS Level 4 Requirements Corrections for NCR Nos. ECSed03495 & ECSed03551 CCR 96-1149A From Baseline 0916

Table 1 - Reference

L4 id	req _ke y	r el	req _typ e	req _sta tus	ver _m etho d	ver _sta tus	C C R	clarifica tion	text	RBR_ id	req_k ey	req_c ategor y	seg me nt	req _typ e	s_v erif _m etho d	s_v erif _sta t	a_v erif _m etho d	a_v erif _sta t	CCR	text	interpret ation text	clarifica tion
S- DP S- 212 30	443 2	A	func tion al	app rov ed	de mo	unv erifi ed		Wallclo ck monitori ng for runawa y PGEs is adequat e while dispatch ing of one PGE per CPU is in affect.	The PRONG CI shall take a predetermined error recovery action if the maximum CPU wallclock time requirements defined for that PGE execution has been exceeded by an adaptable percentage value.	PGS- 0340# A	7398	missi on essen tial	SD PS	func tion al	test	un- veri fied	test	un- veri fied	96- 0968A	The PGS shall utilize fault isolation tools provided by the LSM for the PGS and its subsystems.	"PGS and its subsyst ems" = PDPS, LSM = MSS (MSS provide s the tools used for fault detections).	
										PGS- 0340# B	7402	missi on essen tial	SD PS	func tion al	test	un- veri fied	test	un- veri fied	96- 0968A	The PGS shall utilize fault isolation tools provided by the LSM for the PGS and its subsystems.	"PGS and its subsyst ems" = PDPS, LSM = MSS (MSS provide s the tools used for fault detections).	

S-		Α	func	app	de		The PRONG CI shall	PGS-	7398	missi	SD	func	test	un-	test	un-	96-		"PGS	
₽P	443		tion	rov	mo		take a predetermined	0340#		on	PS	tion		veri		veri	0968A	The PGS shall utilize	and its	
S-	3		al	ed			error recovery action if	A		essen		al		fied		fied		fault isolation tools	subsyst	
212							the maximum memory			tial								provided by the LSM	ems" =	
40							usage requirements											for the PGS and	PDPS,	
1.0							defined for that PGE											its subsystems.	LSM =	
							has been exceeded by												MSS	
							an adaptable												(MSS	
							percentage value.												provide	
							percentage value.												s the	
																			tools	
																			used for	
																			fault	
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<u> </u>								D00	7400		0.0	,					00		ns).	
S-								PGS-	7402	missi	SD	func	test	un-	test	un-	96-	TI DOO ! !! :'''	"PGS	
DP								0340#		on	PS	tion		veri		veri	0968A		and its	
S-								₿		essen		al		fied		fied		fault isolation tools	subsyst	
212										tial								provided by the LSM	ems" =	
40																		for the PGS and	PDPS,	
																		its subsystems.	LSM =	
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																			tools	
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																			fault	
																			detectio	
																			ns).	

Table 2 - Level 4 Deletions and Modifications

L4 id	req_k ey	r el	req_typ e	req_stat us	ver_ metho d	ver _sta tus	C C R	clarification	text
S-DPS- 21230	4432	A	function al	approve d	demo	unv erifi ed		Wallclock monitoring for runaway PGEs is adequate while dispatching of one PGE per CPU is in affect.	The PRONG CI shall take a predetermined error recovery action if the maximum CPU wallclock time requirements defined for that PGE execution has been exceeded by an adaptable percentage value.
S-DPS- 21240	-4433	A	function al	approve d	demo				The PRONG CI shall take a predetermined error recovery action if the maximum memory usage requirements defined for that PGE has been exceeded by an adaptable percentage value.